

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit with integrated memory and data-processing logic portions is provided, with which productivity and yield can be improved. The semiconductor integrated circuit is provided with a memory, a 5 plurality of logic portions that are capable of being connected to the memory, and a separation portion that connects one of the plurality of logic portions to the memory, while separating the other logic portions from the memory. One required logic portion of the plurality of logic portions is connected to the memory, and the other non-required logic portions are separated from the memory, by the separation 10 portion. The semiconductor integrated circuit can be switched for a system LSI after wafer processing, so this enables a plurality of system LSIs to be manufactured using the same exposure masks, thus improving productivity. Furthermore, defective logic portions can be recovered, thus improving the yield.